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perhaps 100 Hz for a motor **control servo**), the computer must read a sensor, ... handles the **sharing** of CPU time among the running tasks. ... The multi-type **FIFO** provide a pseudo-typed transport mechanism for data of different types. ...[trs-new.jpl.nasa.gov/dspace/bitstream/2014/19375/1/69-0725.pdf](http://trs-new.jpl.nasa.gov/dspace/bitstream/2014/19375/1/69-0725.pdf)**Integrated DVD/CD Controller - Patent 6466736**Aug 23, 2010 ... The memory subsystem thus provides efficient **sharing** of memory resources ... Based on the **servo error control** signals, DVD/CD **controller** 62 ...[www.docstoc.com/.../Integrated-DVDCD-Controller--Patent-6466736](http://www.docstoc.com/.../Integrated-DVDCD-Controller--Patent-6466736) - Cached**Presentations: OSADL - Open Source Automation****Development Lab eG**PC-Based Open Architecture **Servo Controller** for CNC Machining, .... supervises timing and wakes up a users space program if data have been put in the **FIFO**. ... **sharing** the data with the normal Linux process through the module of mbuf, ...[www.osadl.org/Presentations.rhws-2000-presentations-0.html](http://www.osadl.org/Presentations.rhws-2000-presentations-0.html) - Cached**PCB buffer, Buffers for PCB handling - FlexLink**PCB buffers. Bookmark and Share. Servo controlled **FIFO** / LIFO buffer.The F1200 **FIFO** / LIFO Buffer utilizes a high speed **servo control** system, ...[www.flexlink.com/en/offering/additional-products/pcb.../buffers.jsp](http://www.flexlink.com/en/offering/additional-products/pcb.../buffers.jsp) - Cached**[PDF] CompactRIO Motor Control Basics Tutorial**

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Click on the Real-Time **FIFO** page and note that the Enable Real-Time **FIFO** box is checked. This enables the high-priority ... Next, click on the stop **shared** variable, create a .... the Brushed DC **Servo Motor Control** (FPGA).vi file. ...[www.ni.com/pdf/labview/us/compactrio\\_motor\\_control\\_basics.pdf](http://www.ni.com/pdf/labview/us/compactrio_motor_control_basics.pdf)**Design of a robot control system architecture - Elsevier**

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Such a synchro- nization provides the means for the **servo control** level to move ... **Sharing** a data structure between a number of concurrent processes is an .... It also has a micro-coded **FIFO** scheduler which supports processes of two ...  
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by W We

Aug 13, 2010 ... management alone because with **FIFO**, a user's **share** of the bandwidth is proportional to the .... (i.e., end-system **control** of the **servo** loop) ...

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real-time part and time-**sharing** part. The real-time part is controlled by RT-Linux kernel and the ... Communicated with RT-**FIFO** and SHM, the two modules form ... mainly include: driver of **servo control** card, driver of pulse current ...

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